



## 9 KHz – 67 GHz Absorptive SPDT Switch

### Description

S001T67 is an ultrawide band (UWB) absorptive single pole double through (SPDT) RF switch. The outputs are terminated to 50  $\Omega$  load in the isolated path. It offers 24 dBm of power handling. The device offers Low voltage logic levels which make this device compatible with CMOS logic. The RF outputs are DC-coupled.

### Features

- Coaxial package
- Low insertion loss
  - 1.5 dB @ 10 GHz
  - 2 dB @ 30 GHz
  - 3 dB @ 40 GHz
  - 4 dB @ 60 GHz
- 15 dB return loss
- Broad temperature operating range
- No low Frequency Spurious emissions
- 40 dB Reverse isolation
- 1.85 mm connectors
- 24 dBm switching power handling



### Applications

- Communication systems
- Datacenters
- Test and Measurement



**Electrical Specifications ( $T_{op}$  23  $\pm$  3°C, unless otherwise specified)**

Parameter	Min.	Typ.	Max.	Unit	Test Conditions
Insertion Loss		1.5		dB	9 KHz – 18 GHz
		2			18 – 30 GHz
		3			30– 45 GHz
		4			45 – 67 GHz
Isolation RF1 to RF2		60		dB	9KHz - 20 GHz
		50			20 – 40 GHz
		40			40 – 67 GHz
Isolation RFC to RF1/RF2	50	55		dB	9 KHz -20 GHz
	35	40			20 – 40 GHz
	30	38			40 – 67 GHz
RFin Return loss		25		dB	9 KHz -20 GHz
		18			20 – 40 GHz
		15			40 – 67 GHz
RF1/RF2 Return loss		20		dB	9 KHz – 20 GHz
		18			20 – 40 GHz
		15			40 – 67 GHz
$T_{ON}$		0.9		$\mu$ s	50% control to 90%
$T_{OFF}$		0.2		$\mu$ s	50% control to 10%
$T_{RISE}$		0.4		$\mu$ s	10% to 90%
$T_{FALL}$		0.06		$\mu$ s	90% to 10%
Input P1dB		28		dBm	10 MHz – 27 GHz
Input IP3		50		dBm	10 MHz – 27 GHz / Two tone, 1 MHz tone separation



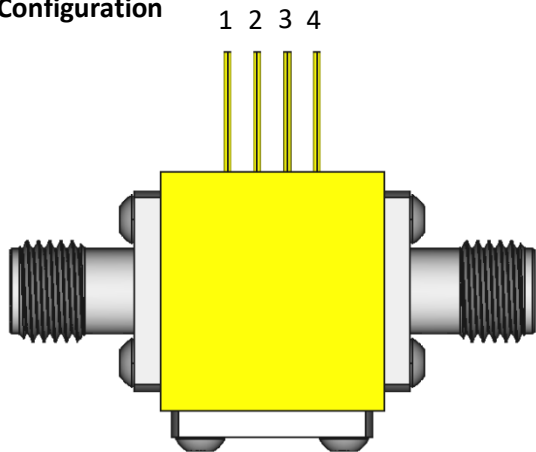
**Absolute Maximum Ratings**

Parameter	Symbol	Condition	Min.	Max.	Unit
VSS	V <sub>SS</sub>		-3.5	0	V
VCC/VC1/VC2			0	3.5	V
Storage Temperature	T <sub>stg</sub>		-65	120	°C
Storage Humidity	H <sub>stg</sub>			85	% r.H.
Operating Temperature	T <sub>op</sub>		-40	85	°C
ESD Susceptibility <sup>1</sup>		HBM		1000	V
Input Power	P <sub>in</sub>	CW		24	dBm

Operating at maximum ratings for a prolonged period will cause damage to the device.

<sup>1</sup> ESD sensitive device, proper ESD protection procedures must be followed.

**Pin Configuration**



Pin Number	Function
1	VSS
2	Control Voltage 1 (V <sub>C1</sub> )
3	Control Voltage 2 (V <sub>C2</sub> )
4	VCC

**Truth Table**

VC1	VC2	RF <sub>out1</sub>	RF <sub>out2</sub>
Low	Low	Off	On
Low	High	On	Off
High	Low	Off	Off
High	High	Off	Off

Off signifies that the RF output is connected to the device 50Ω load  
 On signifies RF out is connected to RF in

### Recommended Bias Sequence

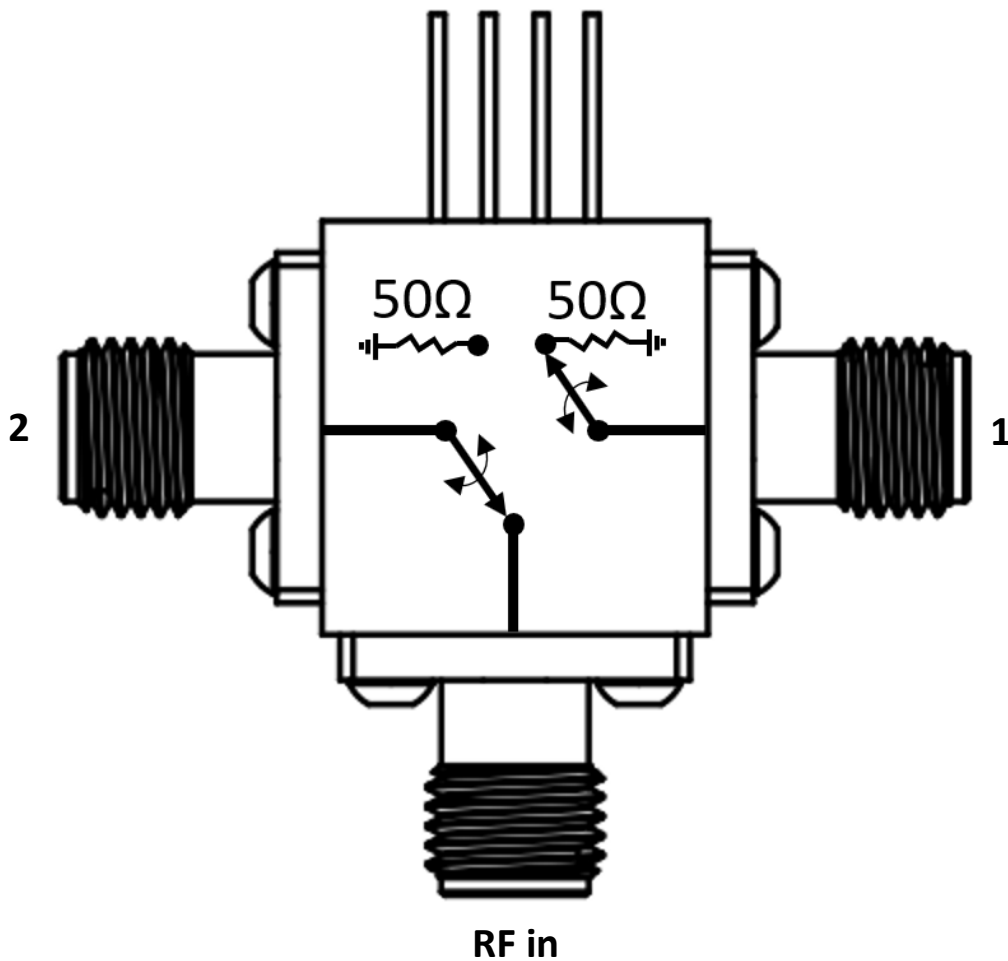
#### Power on:

- 1.) Apply VCC Bias
- 2.) Apply VSS Bias
- 3.) Apply Control Bias

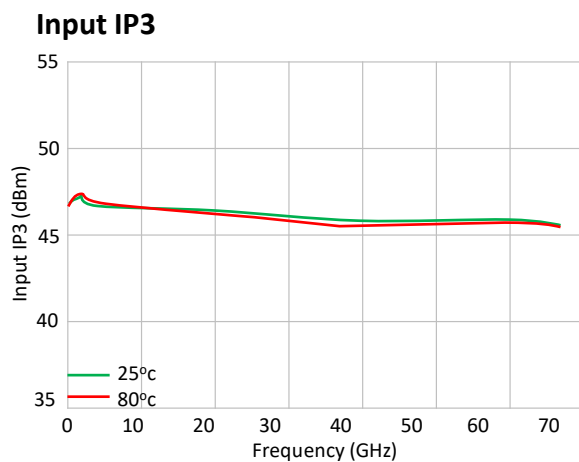
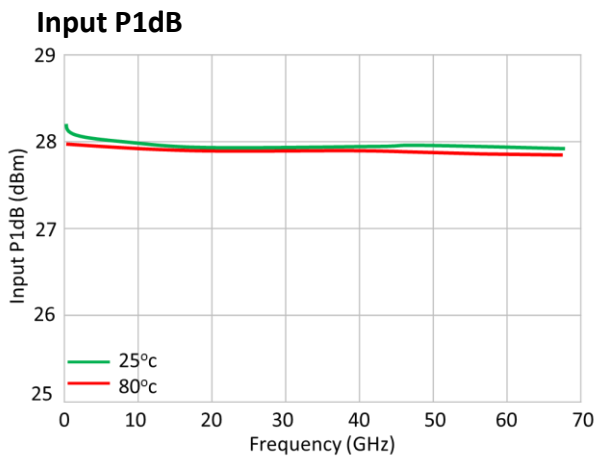
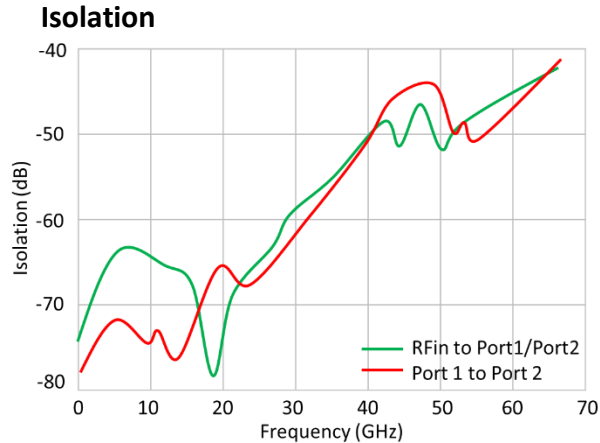
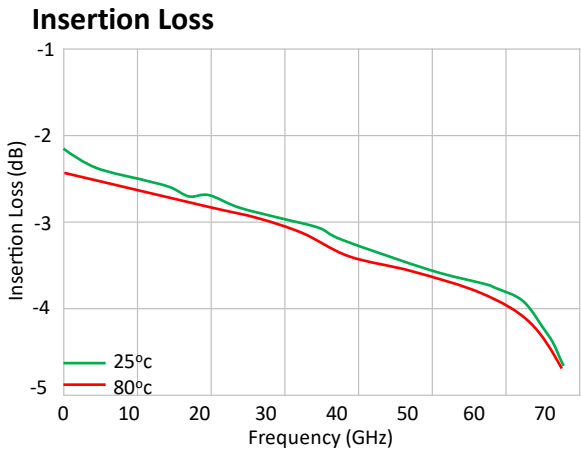
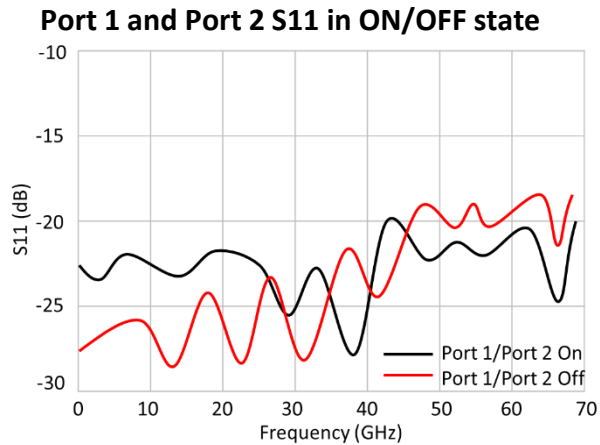
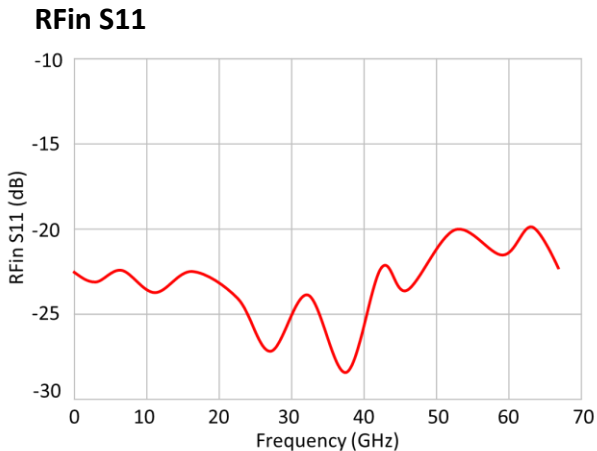
#### Power off:

- 1.) Remove Control Bias
- 2.) Remove VSS Bias
- 3.) Remove VCC Bias

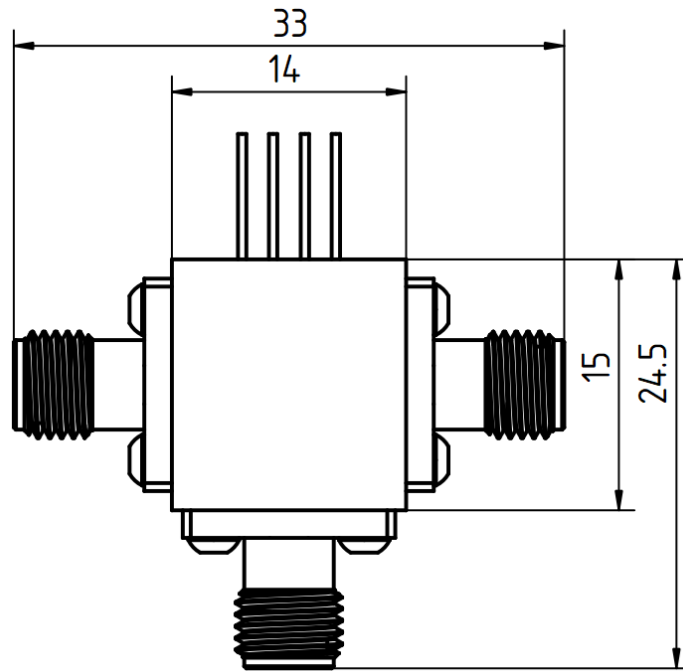
### Functional Diagram



Typical performance curves ( $T_{op} 23 \pm 3^{\circ}C$ )

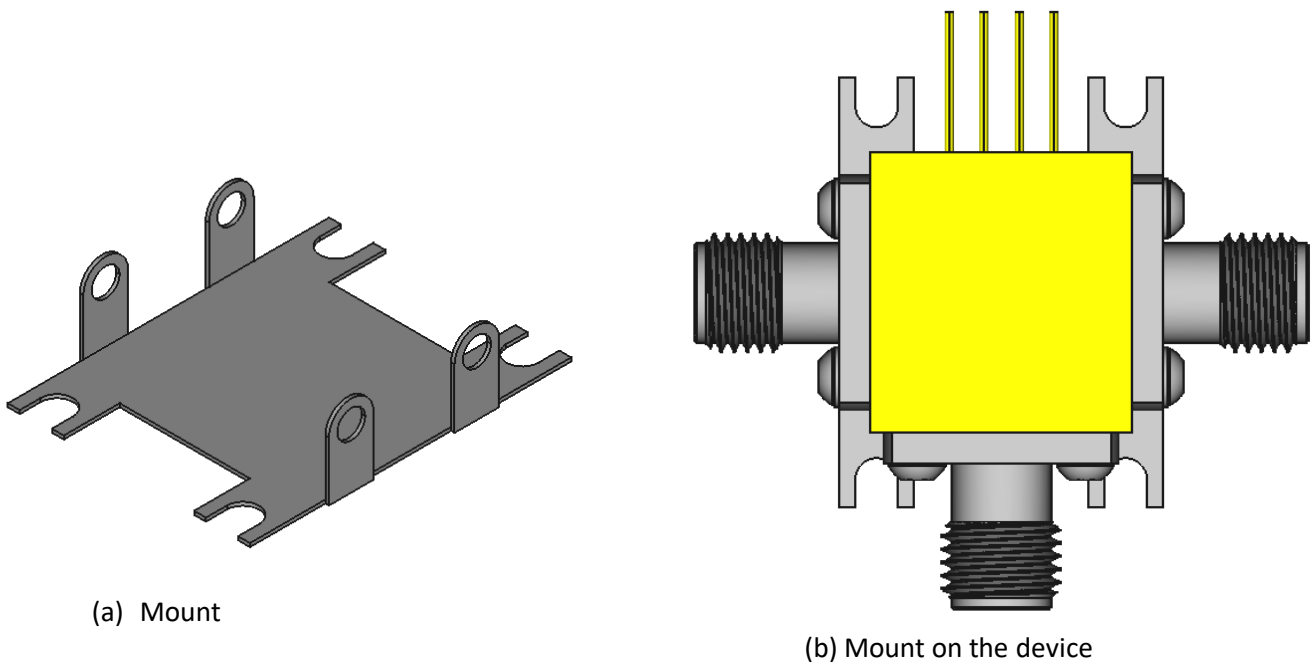


**Device Dimensions (all units in mm)**



*Figure 1. All units in mm. Manufactured device sizes may differ.*

**Optional Mount:** If is desirable to mechanically fix the device in place. An optional Mount can be added to the device.



### **Inquiry Information**

**Sales:** All inquiries regarding sales please contact [Sales@NuPhotonics.com](mailto:Sales@NuPhotonics.com)

**General:** If you are interested in a custom solution, general information, or engineering related information please contact [Inquiry@NuPhotonics.com](mailto:Inquiry@NuPhotonics.com)

## IMPORTANT NOTICES AND DISCLAIMERS

Warranty: NUPHOTONICS PROVIDES ALL OF THE INFORMATION ON TECHNICAL AND RELIABILITY DATA. THIS INCLUDES INFORMATION PRESENTED IN DATA SHEETS, DESIGN FILES, APPLICATIONS, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD-PARTY INTELLECTUAL PROPERTY RIGHTS.

The information and resources are presented and intended for developers that are skilled and adequately qualified to work with this technology. You, the customer, are solely responsible for and accept full responsibility for selecting the appropriate NuPhotonics devices for your application. You accept the sole responsibility of designing, validating, and testing your application. You bear all responsibility for your application meeting standards, safety, security, and other regulatory requirements.

NuPhotonics retains the right to change these resources without notice. All rights are reserved for NuPhotonics. NuPhotonics grants you permission to use the information in these resources to design with NuPhotonics devices. Reproduction and display of these resources is prohibited. No Third-party licenses are offered. You will fully indemnify NuPhotonics against any claims, damages, costs, losses, and liabilities that arise from you using these resources.

NuPhotonics does not accept and objects to any terms you have proposed.

For terms and conditions for all NuPhotonics products please refer to [www.nuphotonics.com](http://www.nuphotonics.com) Legal section.

Definitions: Product State

Alpha Build: Devices in Alpha build are in internal engineering build and testing stages. Major changes may happen for production build.

Beta Build: Devices in Beta build are for external customer and engineering sample testing stages. Minor changes may happen for production build.

Production Build: Customer ready devices. Small appearance changes may occur between devices.

Obsolete: Currently not supported.